Amendments to the Specification:

In the Title, replace the Title with:

A HARDWARE I/O CONTROL BLOCK STRUCTURE FOR MIRRORED AND NON-MIRRORED DATA TRANSFERS

Please add the following <u>new</u> paragraph above the "BACKGROUND OF THE INVENTION" on line 9 of page 1:

RELATED APPLICATION

This application is a divisional of U.S. Patent Application Serial No. 10/051,907, entitled "RAID 1 WRITE MIRRORING METHOD FOR HOST ADAPTERS," of B. Arlen Young filed on January 16, 2002, which issued as U.S. Patent No. xx,xxx,xxx B1 on XXXX, xx, 2004, and which is incorporated herein by reference in its entirety.

Please add the following <u>new</u> paragraph after the paragraph ending on line 3 of page 22.

This application is related to the following divisional applications from the above cited parent application:

1. U.S. Patent Application Serial No. 10/xxx,xxx, (Attorney Docket No. ADPT105102) entitled "A HARDWARE I/O CONTROL BLOCK ARRAY FOR MIRRORED DATA TRANSFERS," of B. Arlen Young filed on February 12, 2004; and

2. U.S. Patent Application Serial No. 10/xxx,xxx, (Attorney Docket No. ADPT105103) entitled "A METHOD FOR CONFIGURING A SINGLE HARDWARE I/O CONTROL BLOCK ARCHITECTURE FOR USE WITH BOTH MIRRORED AND NON-MIRRORED DATA TRANSFERS," of B. Arlen Young filed on February 12, 2004, each of which is incorporated herein by reference in its entirety.